

SARDAR VALLABHBHAI NATIONAL INSTITUTE OF TECHNOLOGY

Ichchhanath, Surat-395007 (Gujarat), India

REPORT SHEET SHOWING RESULT OF CANDIDATES APPEARED AT M.TECH. I FIRST SEMESTER VLSI AND EMBEDDED SYSTEMS AT REGULAR EXAM HELD DURING NOV/DEC 2024

					1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark
Semester	Student Name		Course Code		ECVL101 T	ECVL103 T	ECVL105 T	ECVL107 P	ECVL109 P	ECVL111 T	ECVL117 T								
					3.00	3.00	3.00	3.00	2.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LAB - I	SEMINAR	SEMICONDUCTOR IC TECHNOLOGY (CE-I)	TESTING AND VERIFICATION OF VLSI CIRCUITS (CE-II)								
Semester : 1	RITOBHRATA MANDAL				AB	CC	BB	AB	AB	AB	BB								
	P24VL001	20.00	20.00	8.25	8.25														
Semester : 1	MUSKAN SRIVASTAVA				BC	CC	DD	AB	AB	BC	BB								
	P24VL002	20.00	20.00	7.05	7.05														
Semester : 1	DOKALA V V SRI PAVAN KUMAR				AA	CC	BB	BB	BB	AB	AA								
	P24VL003	20.00	20.00	8.45	8.45														
Semester : 1	GOWTHAM RAJ KHANDAVALLI				BB	CD	BC	AB	BB	CC	BB								
	P24VL004	20.00	20.00	7.25	7.25														
Semester : 1	RAJA BHARGAV				AB	BC	AA	AB	AA	AA	BB								
	P24VL005	20.00	20.00	8.95	8.95														
Semester : 1	AMAN PATEL				BB	CC	DD	AB	AB	BC	BB								
	P24VL006	20.00	20.00	7.20	7.20														
Semester : 1	M VENKATESHWARA RAO				BB	DD	AB	BB	AB	BB	AB								
	P24VL007	20.00	20.00	7.80	7.80														
Semester : 1	SAURAV KUMAR				AB	BC	BC	AB	AB	AA	BB								
	P24VL008	20.00	20.00	8.40	8.40														
Semester : 1	DHANORKAR ARYA DHANANJAY ANITA				BB	BB	CC	AB	AA	BB	BB								
	P24VL009	20.00	20.00	8.05	8.05														
Semester : 1	AKASH ARUN				AB	BB	BC	AB	AB	AB	AB								
	P24VL010	20.00	20.00	8.55	8.55														
Semester : 1	OMMI SRILEKHA				BC	DD	DD	BB	BB	BC	BB								
	P24VL011	20.00	20.00	6.50	6.50														
Semester : 1	LAKKIMSETTI YESHWANTH				BC	DD	BC	BB	AB	CC	BC								
	P24VL012	20.00	20.00	6.75	6.75														
Semester : 1	YENIKA LATHA SRI				CD	DD	DD	BB	BC	CD	BC								
	P24VL013	20.00	20.00	5.65	5.65														
Semester : 1	UMANG MAHENDRABHAI JOSHI				BB	DD	CD	AA	BC	BC	BB								
	P24VL014	20.00	20.00	7.00	7.00														
Semester : 1	CHIKKU NARESH				AB	AB	AA	AB	AB	AB	BC								
	P24VL015	20.00	20.00	8.85	8.85														
Semester : 1	PATEL BRIJESH RAMANBHAI				AB	BC	AA	AA	BB	AB	AA								
	P24VL016	20.00	20.00	9.05	9.05														
Semester : 1	SINOJIYA DIPEN SHASHIKANTBHAI				BB	CD	AB	AA	BB	AB	BC								
	P24VL017	20.00	20.00	8.00	8.00														

Abbreviations : SGPA = Semester Grade Point Average, CGPA = Cumulative Grade Point Average Grade : AA-10 AB-9 BB-8 BC-7 CC-6 CD-5 DD-4 FF-0 II - INCOMPLETE NA - NOT APPEARED XX - UNSATISFACTORY ATTENDANCE NP/NF-AUDIT PASS/FAIL

Date : 30-12-2024

The actual mark statement with the Academic / Examination section shall be final & binding to all

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Semester	Student Name	Course Code			ECVL101 T	ECVL103 T	ECVL105 T	ECVL107 P	ECVL109 P	ECVL111 T	ECVL117 T								
		Credit			3.00	3.00	3.00	3.00	2.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LAB - I	SEMINAR	SEMICONDUCTOR IC TECHNOLOGY (CE-I)	TESTING AND VERIFICATION OF VLSI CIRCUITS (CE-II)								
Semester : 1 SEERAPU VISWESWARARAO					AA	BB	BB	AB	BB	BB	BB								
P24VL018 20.00 20.00 8.45 8.45																			
Semester : 1 KUNISSETTI VISHNU VARDHAN					AB	BC	BB	AB	AB	BB	BB								
P24VL019 20.00 20.00 8.25 8.25																			
Semester : 1 GUNDREDIPALLY SURAJ					BC	CD	CD	BB	AB	BC	BB								
P24VL020 20.00 20.00 6.90 6.90																			
Semester : 1 RUDRA UDAY KIRAN					AA	CC	BB	AB	BC	BB	BC								
P24VL021 20.00 20.00 7.90 7.90																			
Semester	Student Name	Course Code			ECVL101 T	ECVL103 T	ECVL105 T	ECVL107 P	ECVL109 P	ECCS105 T	ECVL117 T								
		Credit			3.00	3.00	3.00	3.00	2.00	3.00	3.00								
Admission Number	Reg-Credit	Earn-Credit	SGPA	CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LAB - I	SEMINAR	ADVANCE DSP (CE-I)	TESTING AND VERIFICATION OF VLSI CIRCUITS (CE-II)								
Semester : 1 JARULLAH MUHAMMED UMAIR MUHAMMED UZAIR					BB	AB	AB	AB	AB	BB	BC								
P24VL022 20.00 20.00 8.40 8.40																			

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Semester	Student Name	Course Code	Credit	1	2	3	4	5	6	7	8	9	10	11	12	13	14	Remark		
				ECVL101 T	ECVL103 T	ECVL105 T	ECVL107 P	ECVL109 P	ECVL111 T	ECVL117 T										
				3.00	3.00	3.00	3.00	2.00	3.00	3.00										
Admission Number	Reg-Credit	Earn-Credit	SGPA CGPA	DIGITAL VLSI DESIGN	MOS DEVICES & TECHNOLOGY	EMBEDDED SYSTEM	VLSI LAB - I	SEMINAR	SEMICONDUCTOR IC TECHNOLOGY (CE-I)	TESTING AND VERIFICATION OF VLSI CIRCUITS (CE-II)										
Semester : 1 KONDREDDY VIJAY SIMHA REDDY																				
P24VL023 20.00 20.00 7.25 7.25				BB	CC	CC	BB	BB	BC	BB										
Semester : 1 OBI REDDY HARSHAVARDHAN REDDY																				
P24VL024 20.00 20.00 8.30 8.30				BB	BB	AB	AB	BB	BB	BB										
Semester : 1 BUYYANA NEERAJ																				
P24VL025 20.00 20.00 8.75 8.75				AA	BB	BB	AB	BB	AB	AB										
Semester : 1 D SHASHIKANTH																				
P24VL026 20.00 20.00 6.60 6.60				BC	DD	CC	BB	AB	CD	BB										
Semester : 1 POLIMARI EKANTH VARDHAN																				
P24VL027 20.00 20.00 7.70 7.70				BB	CC	BB	BB	BB	BB	BB										
Semester : 1 SHIVAM KUMAR																				
P24VL028 20.00 20.00 8.85 8.85				AA	BB	BB	BB	AB	AA	AB										
Semester : 1 SHIVAM PANDEY																				
P24VL029 20.00 20.00 6.90 6.90				BB	CC	CD	BB	AB	BB	CD										
Semester : 1 KATRAVATH GOVIND																				
P24VL030 20.00 14.00 4.25 6.07				CC	FF	DD	BB	BB	FF	CD									BACKLOG : 6 CREDIT(S)	

Total Student Count : 30